

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2163	(real-time same clock same time same reference)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:08
L2	241	(real-time same clock same time adj reference)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:08
L3	6	(real-time same clock same time adj reference same adjustment)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:08
L4	46	(real-time same clock same time same reference same adjustment)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:13
L5	0	(real-time same clock same time same reference same adjustment same microchip)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:13
L6	16	(real-time same clock same time same reference same adjustment same circuit)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:14
L7	0	(real-time same clock same counter same time same reference same adjustment same circuit)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:15
L8	3	(real-time same clock same counter same time same reference same adjustment)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:15
L9	13	(real adj time same clock same counter same time same reference same adjustment)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:16
L10	3561	(real adj time same clock same counter same time adj reference same adjustment)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:17
L11	1	(real adj time same clock same counter same time adj reference same adjustment)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:17
L12	42	(clock same counter same time adj reference same adjustment)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:18
L13	38	(clock same counter same time adj reference same adjustment) and (clock same circuit)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:20
L14	25	(clock same counter same time adj reference same adjustment) and (clock adj circuit)	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:21
L15	0	(clock same counter same time adj reference same adjustment) and (clock adj circuit) and drift and bias	US-PGPUB; USPAT	OR	OFF	2012/08/02 11:22

8/2/2012 11:23:11 AM